VMMK-3213

6 - 18 GHz Directional Detector in SMT Package

Avago TECHNOLOGIES

# Data Sheet

Lead (Pb) Free RoHS 6 fully compliant

Description

The VMMK-3213 is a small and easy-to-use, broadband, directional detector operating in various frequency bands from 6 to 18 GHz with typical insertion loss of 0.4 dB. It is housed in the Avago Technologies' industry-leading and revolutionary sub-miniature chip scale package (GaAsCap wafer scale leadless package) which is small and ultra thin yet can be handled and placed with standard 0402 pick and place assembly equipment. The VMMK-3213 provides a wide detecting power level from -5 to +30 dBm with excellent input and output return losses. A typical of 15 dB directivity is provided, and the detector requires only 1.5 V DC biasing with small current drawn of 0.19 mA.

WLP0402, 1 mm x 0.5 mm x 0.25 mm
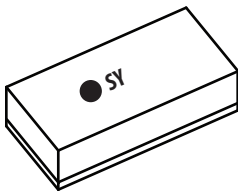
Pin Connections (Top View)
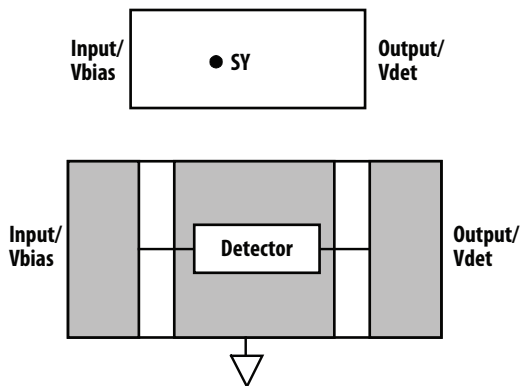
Note:
"S" = Device Code
"Y" = Month Code

Features

- 1 x 0.5 mm surface mount package
- Ultrathin (0.25 mm)
- Wide frequency range: 6 to 18 GHz
- Wide dynamic range
- Low Insertion loss
- Directivity: 15-17 dB typ.
- In and output match: 50 ohm

Specifications (12 GHz, Vb = 1.5 V, Zin = Zout = 50 Ω)

- Bias Current: 0.19 mA typical
- Insertion Loss: 0.3 dB
- Detector output offset voltage: 62 mV typical
- Detector Output voltage at +16 dBm: 559 mV typical

Applications

- Point-to-Point Radio
- Monitoring Power Amplifier Output Power
- Power Control Loop Detector

Attention: Observe precautions for handling electrostatic sensitive devices.
ESD Machine Model = 70 V
ESD Human Body Model = 450 V
Refer to Avago Application Note A004R:
Electrostatic Discharge, Damage and Control.

Electrical Specifications

Table 1. Absolute Maximum Rating [1]

Sym	Parameters/Condition	Unit	Absolute Max
Vbias	Bias Voltage (RF Input)	V	2
Ibias	Bias Current	mA	1
$P_{in, max}$	CW RF Input Power (RF Input) [2]	dBm	+34
Tch	Max Channel Temperature	°C	150

Notes
1. Operation of this device above any one of these parameters may cause permanent damage
2. With the DC (typical bias) and RF applied to the device at board temperature, Tb = 25° C

Table 2. DC and RF Specifications

T_A = 25° C, Freq = 12 GHz, V_b = 1.5 V, Z_{in} = Z_{out} = 50 Ω unless otherwise specified

| Symbol	Parameters / Condition	Unit	Min	Typical	Max
Ibias [1]	Bias Current	mA	0.11	0.19	0.25
I.L. [1]	Insertion Loss at 6 GHz at 12 GHz at 18 GHz	dB		 0.15 0.3 0.5	
IRL [1]	Input Return Loss	dB		20	
ORL [1]	Output Return Loss	dB		20	
Dir [2]	Directivity at 6 GHz at 12 GHz at 18 GHz	dB		 15 15 17	
Voffset [1,3]	Detector Output Offset Voltage	mV	45	62	75
Vdet [4]	Detector Output Voltage at +16 dBm	mV	460	559	700

Notes
1. Measured data obtained from wafer-probing, losses from measurement system de-embedded from final data, Vbias = 1.5 V applied through a broadband bias tee.
2. Measured by reversing the detector and applying RF power to the output port. Directivity is defined as the difference in dB between the power applied in the forward direction and the power required in the reverse direction to produce the same Vdet voltage.
3. Voffset is measured with RF input power turned off.
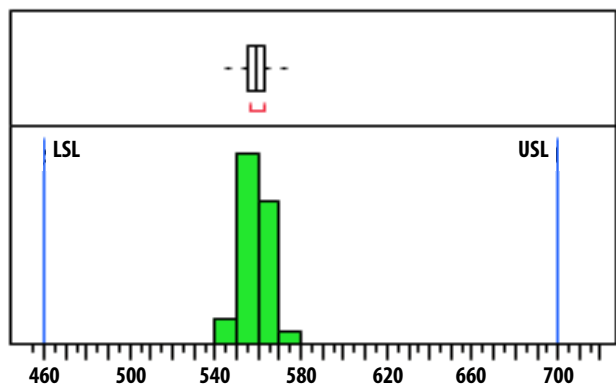4. Vdet is measured with +16 dBm RF input power at 12 GHz.

Product Consistency Distribution Charts at 12 GHz, Vbias = 1.5 V
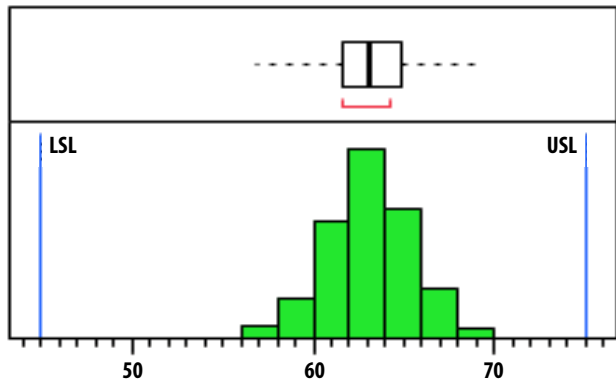
Ibias: Mean = 0.19 mA, LSL = 0.11 mA, USL = 0.25 mA
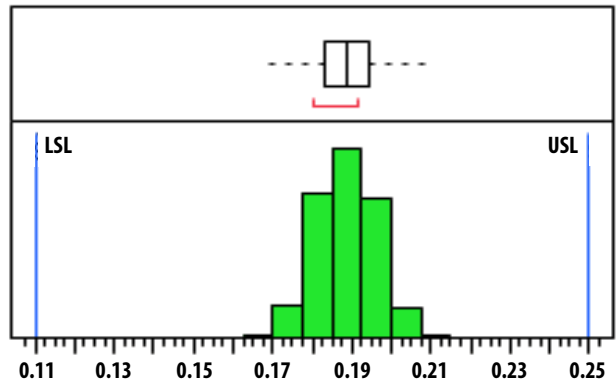
Vdet_Off: Mean = 62 mV, LSL = 45 mV, USL = 75 mV

Vdet_On @Pin = +16 dBm: Mean = 559 mV, LSL = 460 mV, USL = 700 mV

Notes:
Distribution data sample sized is based on at least 54 Kpcs taken from MPV lots.
Future wafers allocated to this product may have nominal values anywhere between the upper and lower limits.

3

VMMK-3213 Typical Performance

S-parameter data obtained using 300 μm G-S-G probe substrate; bias was brought in via broadband bias tees. Power vs. Vdet data obtained using CPW PCB (Fig.8). Losses calibrated out to the package reference plane.
(T_A = 25° C, Vbias = 1.5 V, Ibias = 0.14 mA, Z_{in} = Z_{out} = 50 Ω unless otherwise specified)
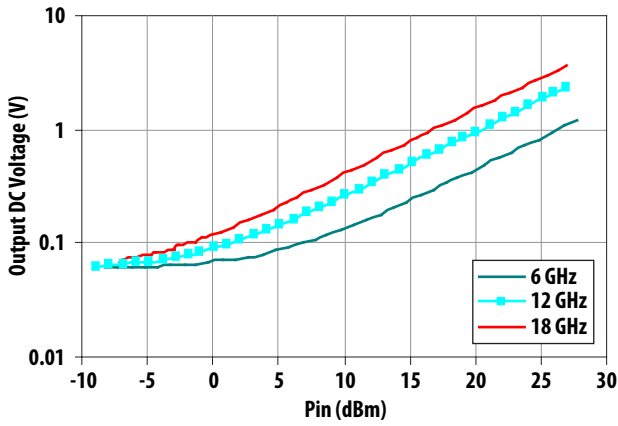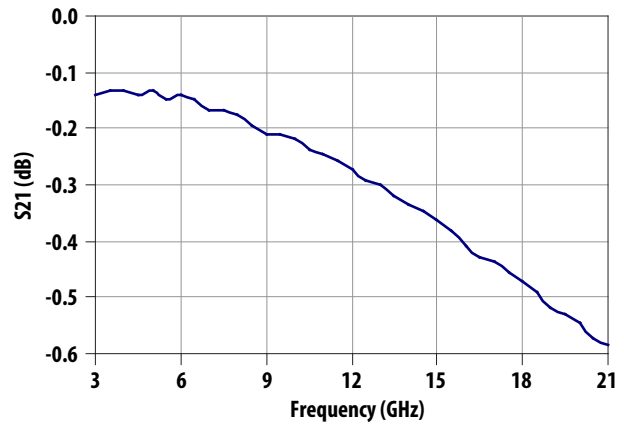
Figure 1. Vdet vs. Input Power

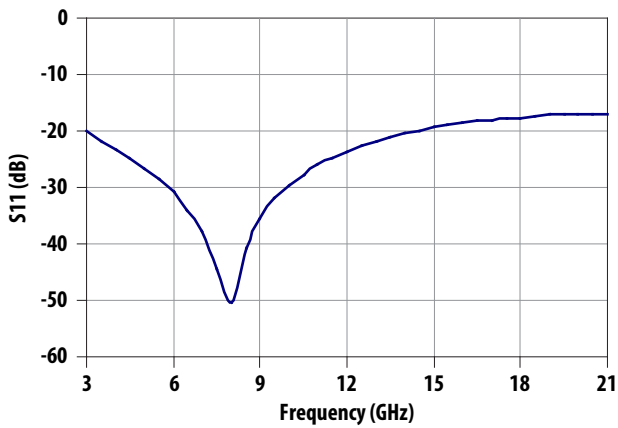Figure 2. Insertion Loss vs. Frequency
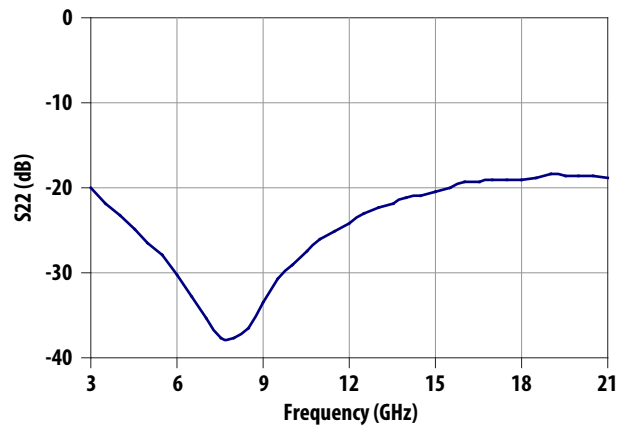
Figure 3. Input Return Loss
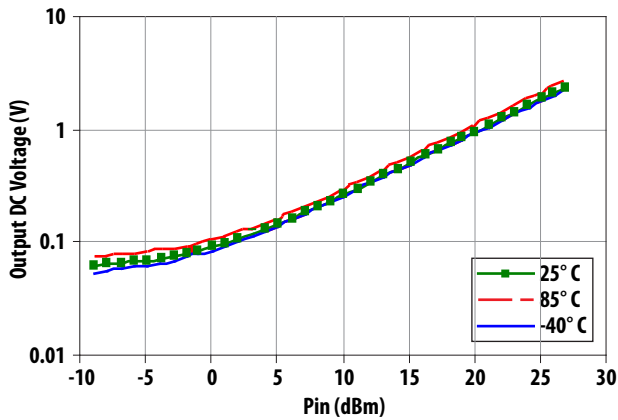
Figure 4. Output Return Loss
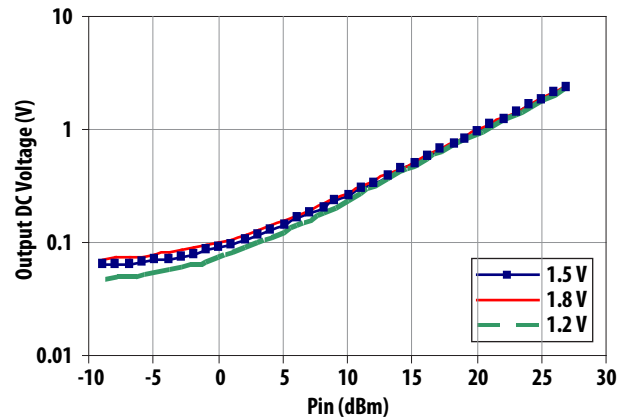
Figure 5. Pin vs. Vdet Over Temperature at 12 GHz

Figure 6. Pin vs. Vdet Over Vbias at 12 GHz

Typical Scattering Parameters

Data obtained with 300 μm G-S-G probing on 0.016 inch thick PCB substrate, broadband bias tees, losses calibrated out to the package reference plane. T_A = 25° C, Z_{in} = Z_{out} = 50 Ω.

Freq	S11			S21			S12			S22		
GHz	dB	Mag	Phase	dB	Mag	Phase	dB	Mag	Phase	dB	Mag	Phase
2	-16.329	0.153	-91.071	-0.193	0.978	-1.382	-0.191	0.978	-1.269	-16.233	0.154	-94.000
2.5	-18.453	0.120	-95.153	-0.155	0.982	-5.835	-0.148	0.983	-5.737	-18.259	0.122	-99.569
3	-20.202	0.098	-98.690	-0.140	0.984	-9.650	-0.134	0.985	-9.541	-20.131	0.099	-104.738
3.5	-21.884	0.081	-102.503	-0.134	0.985	-13.124	-0.123	0.986	-13.004	-21.788	0.081	-109.162
4	-23.414	0.068	-105.623	-0.131	0.985	-16.405	-0.126	0.986	-16.331	-23.223	0.069	-113.696
4.5	-24.928	0.057	-108.776	-0.137	0.984	-19.521	-0.130	0.985	-19.488	-24.867	0.057	-120.187
5	-26.745	0.046	-109.909	-0.135	0.985	-22.562	-0.135	0.985	-22.471	-26.558	0.047	-125.296
5.5	-28.473	0.038	-113.480	-0.145	0.983	-25.529	-0.136	0.985	-25.479	-27.959	0.040	-132.170
6	-30.903	0.029	-115.590	-0.140	0.984	-28.456	-0.145	0.984	-28.393	-30.117	0.031	-141.254
6.5	-33.807	0.020	-117.357	-0.147	0.983	-31.316	-0.147	0.983	-31.263	-32.803	0.023	-151.930
7	-37.788	0.013	-119.232	-0.165	0.981	-34.175	-0.155	0.982	-34.135	-35.289	0.017	-167.484
7.5	-44.437	0.006	-114.385	-0.170	0.981	-36.984	-0.171	0.981	-36.908	-37.721	0.013	165.421
8	-52.041	0.003	4.805	-0.178	0.980	-39.787	-0.177	0.980	-39.765	-37.523	0.013	128.405
8.5	-41.310	0.009	43.715	-0.191	0.978	-42.527	-0.183	0.979	-42.529	-36.250	0.015	100.837
9	-35.442	0.017	46.489	-0.208	0.976	-45.339	-0.195	0.978	-45.325	-33.681	0.021	82.253
9.5	-31.601	0.026	42.448	-0.208	0.976	-48.168	-0.213	0.976	-48.081	-30.873	0.029	69.094q
10	-29.551	0.033	41.801	-0.220	0.975	-50.893	-0.222	0.975	-50.865	-29.168	0.035	60.256
10.5	-27.702	0.041	39.768	-0.238	0.973	-53.656	-0.228	0.974	-53.600	-27.576	0.042	55.206
11	-26.090	0.050	35.896	-0.247	0.972	-56.432	-0.241	0.973	-56.373	-26.090	0.050	50.804
11.5	-24.822	0.057	32.759	-0.258	0.971	-59.141	-0.264	0.970	-59.104	-25.256	0.055	45.751
12	-23.702	0.065	29.720	-0.273	0.969	-61.903	-0.272	0.969	-61.880	-24.194	0.062	41.302
12.5	-22.522	0.075	27.418	-0.291	0.967	-64.668	-0.291	0.967	-64.639	-23.148	0.070	37.716
13	-21.788	0.081	24.763	-0.300	0.966	-67.435	-0.304	0.966	-67.363	-22.361	0.076	35.261
13.5	-21.180	0.087	21.869	-0.317	0.964	-70.131	-0.318	0.964	-70.112	-21.798	0.081	31.227
14	-20.436	0.095	20.216	-0.335	0.962	-72.913	-0.330	0.963	-72.864	-21.230	0.087	29.186
14.5	-20.026	0.100	17.762	-0.346	0.961	-75.675	-0.346	0.961	-75.643	-20.857	0.091	26.370
15	-19.437	0.107	15.451	-0.366	0.959	-78.383	-0.362	0.959	-78.305	-20.373	0.096	22.316
15.5	-18.931	0.113	12.742	-0.382	0.957	-81.127	-0.394	0.956	-81.065	-19.922	0.101	19.575
16	-18.431	0.120	10.976	-0.406	0.954	-83.817	-0.402	0.955	-83.806	-19.348	0.108	16.130
16.5	-18.259	0.122	8.693	-0.425	0.952	-86.562	-0.421	0.953	-86.573	-19.348	0.108	13.556
17	-18.174	0.123	6.897	-0.439	0.951	-89.307	-0.443	0.950	-89.310	-19.196	0.110	11.590
17.5	-17.829	0.128	5.621	-0.454	0.949	-92.036	-0.457	0.949	-92.074	-19.016	0.112	8.708
18	-17.808	0.129	3.248	-0.478	0.947	-94.816	-0.473	0.947	-94.802	-19.117	0.111	6.649
18.5	-17.400	0.135	0.833	-0.493	0.945	-97.581	-0.493	0.945	-97.553	-18.786	0.115	2.818
19	-17.071	0.140	-1.099	-0.518	0.942	-100.267	-0.519	0.942	-100.250	-18.431	0.120	0.679
19.5	-17.077	0.140	-3.077	-0.533	0.941	-103.024	-0.530	0.941	-103.033	-18.518	0.119	-0.867
20	-17.040	0.141	-4.701	-0.550	0.939	-105.736	-0.557	0.938	-105.744	-18.607	0.117	-2.826

Typical Scattering Parameters

Data obtained with 300 μm G-S-G probing on 0.016 inch thick PCB substrate, broadband bias tees, losses calibrated out to the package reference plane. $T_A = 25°$ C, $Z_{in} = Z_{out} = 50$ Ω.

Freq GHz	S11 dB	Mag	Phase	S21 dB	Mag	Phase	S12 dB	Mag	Phase	S22 dB	Mag	Phase
20.5	-17.040	0.141	-7.217	-0.574	0.936	-108.456	-0.573	0.936	-108.468	-18.614	0.117	-4.134
21	-17.028	0.141	-8.738	-0.588	0.935	-111.208	-0.588	0.935	-111.185	-18.938	0.113	-6.533
21.5	-17.102	0.140	-11.285	-0.615	0.932	-113.916	-0.602	0.933	-113.999	-18.839	0.114	-8.197
22	-16.767	0.145	-14.377	-0.631	0.930	-116.663	-0.624	0.931	-116.666	-18.644	0.117	-11.441
22.5	-16.912	0.143	-15.091	-0.647	0.928	-119.473	-0.653	0.928	-119.486	-18.816	0.115	-10.582
23	-17.158	0.139	-17.004	-0.668	0.926	-122.140	-0.663	0.927	-122.167	-19.008	0.112	-12.340
23.5	-17.184	0.138	-19.299	-0.692	0.923	-124.960	-0.688	0.924	-124.930	-19.372	0.108	-14.422
24	-17.426	0.135	-19.770	-0.691	0.924	-127.701	-0.699	0.923	-127.677	-19.752	0.103	-14.094
24.5	-17.361	0.136	-24.551	-0.724	0.920	-130.446	-0.722	0.920	-130.459	-20.044	0.100	-19.320
25	-17.438	0.134	-27.736	-0.743	0.918	-133.225	-0.749	0.917	-133.215	-20.158	0.098	-19.404
25.5	-17.438	0.134	-28.363	-0.758	0.916	-135.945	-0.758	0.916	-135.971	-20.131	0.099	-18.395
26	-17.849	0.128	-31.343	-0.774	0.915	-138.759	-0.780	0.914	-138.714	-20.364	0.096	-17.914
27	-18.153	0.124	-36.406	-0.817	0.910	-144.424	-0.810	0.911	-144.417	-20.983	0.089	-17.550
28	-18.034	0.125	-47.145	-0.845	0.907	-149.960	-0.851	0.907	-149.888	-21.280	0.086	-26.025
29	-18.013	0.126	-50.732	-0.888	0.903	-155.543	-0.890	0.903	-155.512	-20.621	0.093	-21.672
30	-18.301	0.122	-58.745	-0.930	0.899	-161.117	-0.938	0.898	-161.139	-21.022	0.089	-22.729
31	-17.822	0.129	-69.913	-0.989	0.892	-167.131	-0.984	0.893	-167.166	-20.915	0.090	-28.168
32	-17.400	0.135	-76.335	-1.036	0.888	-172.821	-1.034	0.888	-172.878	-20.593	0.093	-28.870
34	-15.874	0.161	-94.500	-1.158	0.875	175.434	-1.149	0.876	175.501	-19.445	0.107	-46.041
36	-14.694	0.184	-109.200	-1.262	0.865	163.850	-1.294	0.862	163.967	-18.482	0.119	-59.354

VMMK-3213 Biasing Information

Biasing and Operation

The VMMK-3213 is a 3 terminal device consisting of a "through" 50 ohm line connecting directly between the RF Input and RF Output ports, and a directional coupler with a full wave detector that provides a DC output proportional to RF power input. As with any high frequency device, good grounding is required on the common port under the device for it to produce low loss in the "through" mode. A suggested PCB layout with appropriate grounding will be cover later in the application section.

With only 3 terminals available, the DC bias and detected voltage are internally DC coupled to the input and output terminals respectively. The key to successful operation of the VMMK-3213 is the use of low loss bias decoupling networks connected to both the RF Input and the RF Output ports. Figure 7 shows a simple biasing circuit.

The bias decoupling networks provide a low loss AC coupled RF path to the device, a means of biasing the device on the input, and a means of extracting the detected voltage on the output of the device. Bias decoupling networks in the 6 to 18 GHz frequency range can be easily produced using simple lumped resistors and lumped capacitors. All SMT components are suggested to be of 0402 or 1005 size. The detector needs two DC blocking caps, C1 and C2, on the input and output ports. This can be accomplished by using SMT capacitors with values chosen for the frequency of operation; e.g. 1 pF is suggested for 10-14 GHz operation. Nominal bias voltage of 1.5 V or 0.14 mA is required for proper operation. Biasing on the input is by a way of a large value resistor R1. Its value can be computed using the following equation:
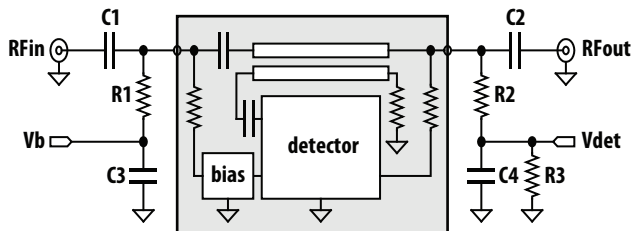
$R1 = (Vb - 1.5) / 0.00014$

where Vb is the supply voltage.

Detected dc voltage is extracted on the output by a way of a large value resistor R2, in the range of 10 kΩ. Bypassing capacitors C3 and C4 are needed to prevent RF influence on the dc lines. Suggested value for bypass capacitors is 1 pF.

At zero RF input power, and at 1.5 V supply bias, a nominal 62 mV offset voltage appears at the detected output port. The internal output source resistance for the detector is approximately 20 kΩ. Resistor R3 can be used as an external load resistor for the detector. Its value can be optimized for the desired Vout vs. RF input curve.

Figure 8 shows a photo of a VMMK-3213 populated PCB used to obtain the Vdet vs. Input Power characterization data from 6 to 18 GHz. For ease in broadband characterization, no on board DC blocking caps were present; instead, external broadband dc blocking capacitors (in 3.5 mm connectors) were used.

Component	Description
C1, C2	0.5 pF to 2 pF
R1	(Vb -1.5) / 0.00014 Ω
R2	10 kΩ
C3, C4	1 pF to 2 pF
R3	External load resistor (optional)

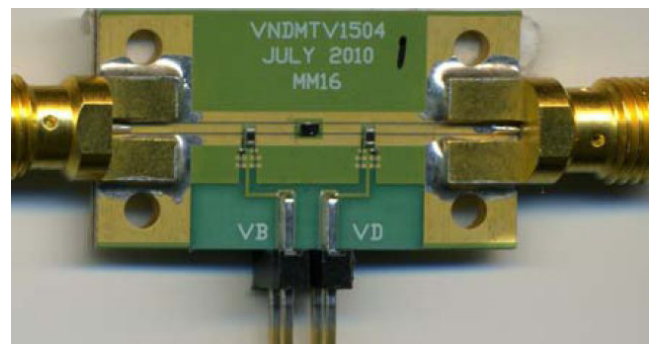Figure 7. Biasing the VMMK-3213 Detector Module

Figure 8. VMMK-3213 Characterization Board
(Note: Need two external DC blocking caps)

S Parameter Measurements

The S-parameters are measured on a .016 inch thick RO4003 printed circuit test board, using 300 μm G-S-G (ground signal ground) probes. Coplanar waveguide is used to provide a smooth transition form the probes to the device under test. The presence of the ground plane on top of the test board results in excellent grounding at the device under test. A combination of SOLT (Short – Open – Load – Thru) and TRL (Thru - Reflect - Line) calibration techniques are used to correct for the effects of the test board, resulting in accurate device S parameters.

Package and Assembly Notes

For detailed description of the device package and assembly notes, please refer to Application Note 5378.

ESD Precautions

Note: These devices are ESD sensitive. The following precautions are strongly recommended. Ensure that an ESD approved carrier is used when die are transported from one destination to another. Personal grounding is to be worn at all times when handling these devices. For more detail, refer to Avago Application Note A004R: Electrostatic Discharge Damage and Control.

Ordering Information

Part Number	Devices Per Container	Container
VMMK-3213-BLKG	100	Antistatic Bag
VMMK-3213-TR1G	5000	7" Reel

Package Dimension Outline
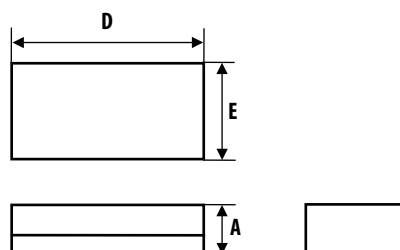
Dimensions Symbol	Min (mm)	Max (mm)
E	0.500	0.585
D	1.004	1.085
A	0.225	0.275

Note:
All dimensions are in mm

Reel Orientation

REEL

USER FEED DIRECTION

CARRIER TAPE

Device Orientation

USER FEED DIRECTION

4 mm

8 mm

TOP VIEW

END VIEW
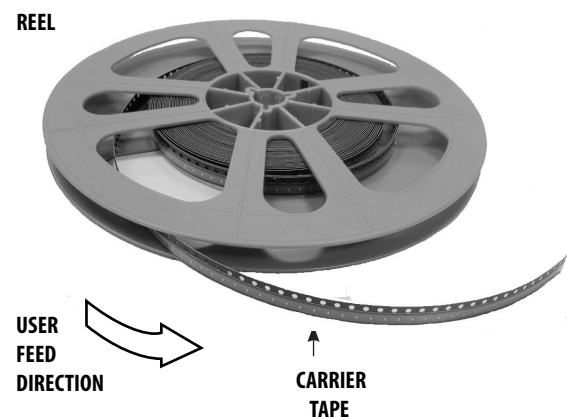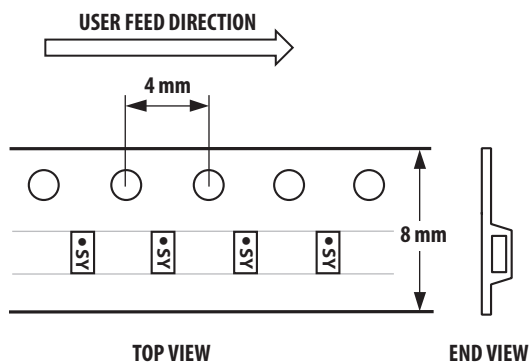
Notes:
"S" = Device Code
"Y" = Month Code

Tape Dimensions

Scale 5:1
A−A SECTION

Ao = 0.73±0.05 mm

Bo = 1.26±0.05 mm

Ko = 0.35 $^{+0.05}_{+0}$ mm

Scale 5:1
B−B SECTION
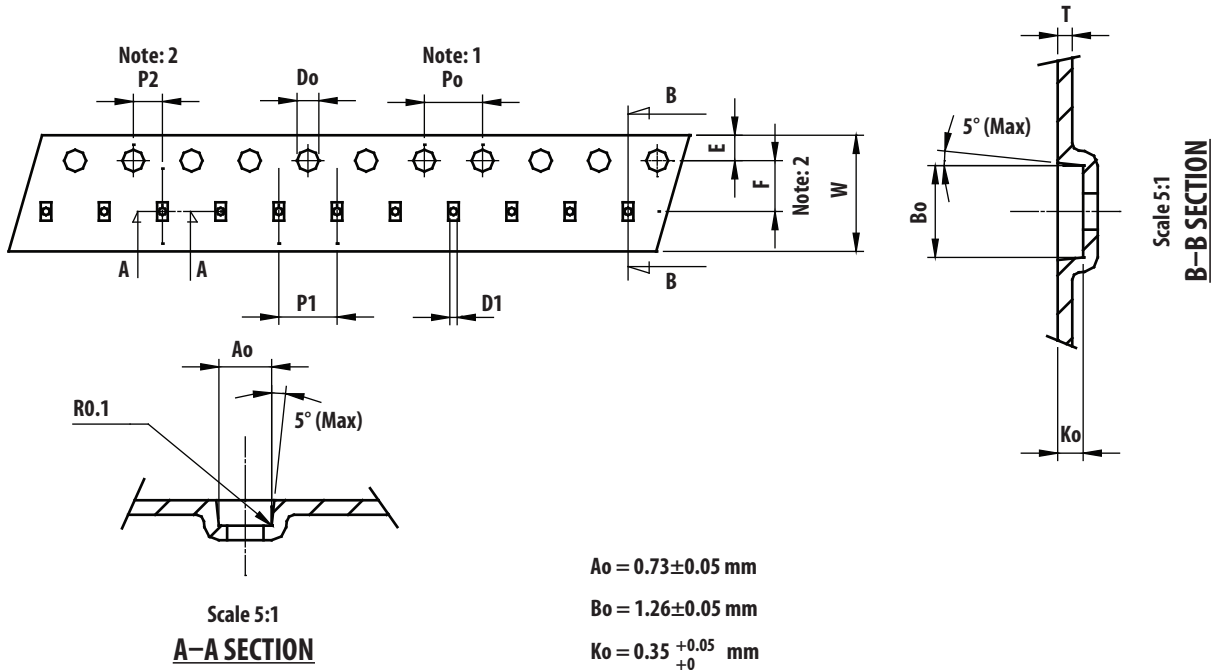
Unit: mm

Symbol	Spec.
K1	–
Po	4.0±0.10
P1	4.0±0.10
P2	2.0±0.05
Do	1.55±0.05
D1	0.5±0.05
E	1.75±0.10
F	3.50±0.05
10Po	40.0±0.10
W	8.0±0.20
T	0.20±0.02

Notice:
1. 10 Sprocket hole pitch cumulative tolerance is ±0.1 mm.
2. Pocket position relative to sprocket hole measured as true position of pocket not pocket hole.
3. Ao & Bo measured on a place 0.3 mm above the bottom of the pocket to top surface of the carrier.
4. Ko measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
5. Carrier camber shall be not than 1 m per 100 mm through a length of 250 mm.

Avago
T E C H N O L O G I E S

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